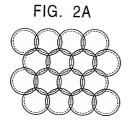
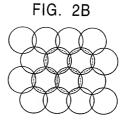
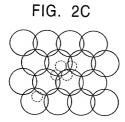
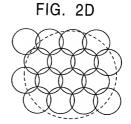


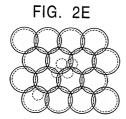
FIG.











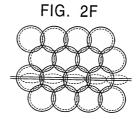


FIG. 3

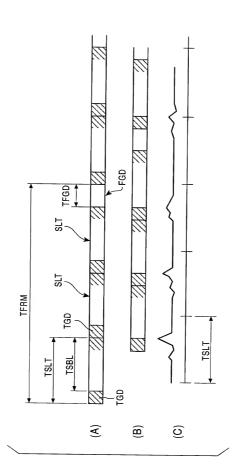


FIG. 4

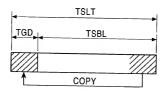


FIG. 5

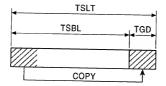
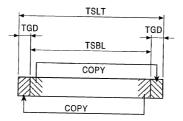
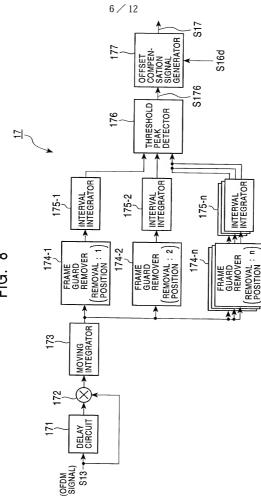


FIG. 6



DEMODU-LATOR 2 ~S17 **S14** SYNCHRONI-ZATION POSITION DETECTOR 댎 위 **S13** FIG. 7 S16b S16c A/D CONVERTER S16d S16a S12,1F CLOCK/ TIMING CONTROLLER DOWN 42 9 S11,RF BAND PASS FILTER RECEIVED OFDM — SIGNAL



=1<u>G</u>.8

FIG. 9

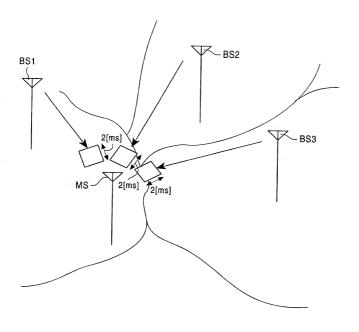


FIG. 10

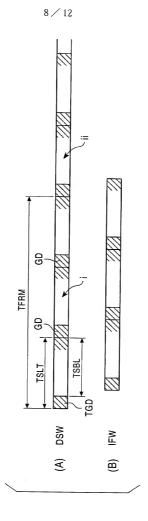


FIG. 11

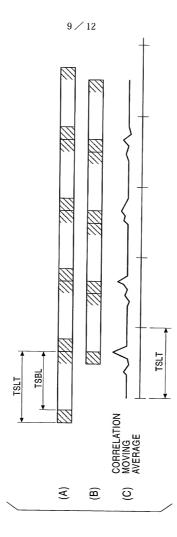


FIG. 12

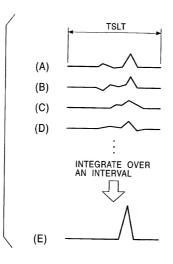


FIG. 13

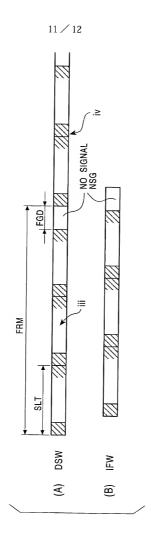


FIG. 14

